UAS Light-weight power-efficient circuit design
Reconfigurable Computing Lab, Electrical Engineering

- **Design and explore low power, light-weight, area-efficient customized architectures for embedded sensing, and shape morphing systems**

- **Optimizations to reduce power and area consumption**
  - **Customized functional units**
  - **Customized communication links**
  - **Homogeneous and heterogeneous architectures**

- **Dynamic / partial reconfiguration**

- **Designing fault-tolerant architectures**
  - **Fault-tolerance for both sensing and processing phases**

Integration in UAS design - Light-weight low power circuit design for embedded sensing, and shape morphing systems