Parasitic-aware Metamodeling
Design Flow (US Patent 9,026,964)

Parasitic-Aware Metamodeling
- Real-time design exploration of very large AMS designs
- Metamodel re-use
- 10,000x speedup over traditional simulation with 2% accuracy

VLSI Multimedia Architectures for the IoT
- First-ever hardware architecture for the BPG image format
- Incorporation of safety features at the source
- Targeted towards the IoT